From 17527158537069c390859441aae57806f89c508a Mon Sep 17 00:00:00 2001

From: harinchandu <harin.chandu@gmail.com>

Date: Thu, 6 Oct 2016 09:47:02 +0530

Subject: [PATCH] HMI\_TOUCH PANEL

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Makefile | 2 +-

board/ti/am335x/mux.c | 18 ++++++++++++------

2 files changed, 13 insertions(+), 7 deletions(-)

diff --git a/Makefile b/Makefile

index f4b3636..fca7ee8 100644

--- a/Makefile

+++ b/Makefile

@@ -5,7 +5,7 @@

VERSION = 2016

PATCHLEVEL = 09

SUBLEVEL = HARIN

-EXTRAVERSION = -rc1

+EXTRAVERSION = I2C1

NAME =

# \*DOCUMENTATION\*

diff --git a/board/ti/am335x/mux.c b/board/ti/am335x/mux.c

index 66dbe1e..88d1f84 100644

--- a/board/ti/am335x/mux.c

+++ b/board/ti/am335x/mux.c

@@ -112,7 +112,7 @@ static struct module\_pin\_mux i2c0\_pin\_mux[] = {

{-1},

};

-static struct module\_pin\_mux i2c1\_pin\_mux[] = {

+static struct module\_pin\_mux i2c1\_pin\_mux[] = { ///\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*i2c1\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*//

{OFFSET(spi0\_d1), (MODE(2) | RXACTIVE |

PULLUDEN | SLEWCTRL)}, /\* I2C\_DATA \*/

{OFFSET(spi0\_cs0), (MODE(2) | RXACTIVE |

@@ -139,10 +139,10 @@ static struct module\_pin\_mux gpio0\_18\_pin\_mux[] = {

{OFFSET(usb0\_drvvbus), (MODE(7) | PULLUDEN)}, /\* GPIO0\_18 \*/

{-1},

};

-static struct module\_pin\_mux gpio0\_30\_pin\_mux[] = {

- {OFFSET(gpmc\_wait0), (MODE(7))}, /\* GPIO0\_30\*\*\*\*\*\*\*I\_ADDED\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\* \*/

- {-1},

-};

+//static struct module\_pin\_mux gpio0\_30\_pin\_mux[] = {

+// {OFFSET(gpmc\_wait0), (MODE(7))}, /\* GPIO0\_30\*\*\*\*\*\*\*I\_ADDED\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\* \*/

+// {-1},

+//};

//static struct module\_pin\_mux gpio0\_31\_pin\_mux[] = {

// {OFFSET(gpmc\_wpn), (MODE(7)|PULLUDEN|PULLDOWN\_EN|RXACTIVE)}, /\* GPIO0\_31\*\*\*I\_ADDED\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\* \*/

// {-1},

@@ -312,6 +312,11 @@ void enable\_i2c0\_pin\_mux(void)

configure\_module\_pin\_mux(i2c0\_pin\_mux);

}

+//void enable\_i2c1\_pin\_mux(void) //\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*i2c1\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*//

+//{

+// configure\_module\_pin\_mux(i2c1\_pin\_mux);

+//}

+

/\*

\* The AM335x GP EVM, if daughter card(s) are connected, can have 8

\* different profiles. These profiles determine what peripherals are

@@ -348,7 +353,8 @@ static unsigned short detect\_daughter\_board\_profile(void)

void enable\_board\_pin\_mux(void)

{

- configure\_module\_pin\_mux(gpio0\_30\_pin\_mux);

+ configure\_module\_pin\_mux(i2c1\_pin\_mux);

+// configure\_module\_pin\_mux(gpio0\_30\_pin\_mux);

//configure\_module\_pin\_mux(gpio0\_31\_pin\_mux);

/\* Do board-specific muxes. \*/

if (board\_is\_bone()) {

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1.9.1